

Amendments to the Specification:

Please replace the paragraph beginning at page 13, line 13 with the following amended paragraph:

Next, a driving method of the pixel shown in FIG. 4A is described. The operation of the pixel shown in FIG. 4A can be divided into a writing period and a holding period. First, in the writing period, when the scan line  $G_j$  ( $j = 1$  to  $y$ ) is selected, the switching transistor 402 whose gate is connected to the scan line  $G_j$  ( $j = 1$  to  $y$ ) is turned ON. Then, a video signal inputted to signal lines  $S_i$  to  $S_x$  is inputted to the gate of the current controlling transistor 404 through the switching transistor 402. It should be noted that the driving transistor 403 is constantly ON since its gate is connected to the ~~first~~second power supply line  $[[V_i]]W_i$  ( $i = 1$  to  $x$ ).

Please replace the paragraph beginning at page 18, line 8 with the following amended paragraph:

**[Formula 1]**

